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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee: Liaw
Serial No.: 10/723,331
Patent.: 7,023,056 B2
For: Memory Cell Structure

Docket No.: TSM03-1154
Filed: November 26, 2003
Issue Date: April 4, 2006

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: July 10, 2006

I hereby certify that the below listed correspondence is being deposited with the United States Postal Service on the date indicated above as first class mail in an envelope addressed to: Certificate of Corrections Branch, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Certificate of Mailing via First Class Mail (1 page)
Request for Issuance of Certificate of Correction (1 page)
Certificate of Correction (1 original and 1 copy)
Return Postcard

Respectfully submitted,

Kristin R. Hayes
Legal Assistant

Slater & Matsil, L.L.P.
17950 Preston Rd., Suite 1000
Dallas, TX 75252
Tel: 972-732-1001
Fax: 972-732-9218

Certificate
JUL 19 2006
of Correction

JUL 20 2006



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee: Liaw

Docket No.: TSM03-1154

Serial No.: 10/723,331

Art Unit: 2818

Patent No.: 7,023,056 B2

Issue Date: April 4, 2006

Filed: November 26, 2003

Examiner: Ngan V. Ngo

For: **Memory Cell Structure**

Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Request for Issuance of Certificate of Correction
Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322

Dear Sir:

Upon review of the above-identified issued patent, Patentee notes errors in the claims that should be corrected as shown on the attached Certificate of Correction.

The mistakes noted on the attached Certificate of Correction were incurred through the fault of the Patent and Trademark Office. Accordingly, no fee is required.

Patentee respectfully solicits the issuance of the requested Certificate of Correction.

Respectfully submitted,

July 10, 2006

Date _____

SLATER & MATSIL, L.L.P.
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~~Roger C. Knapp~~
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Reg. No. 46,836

JUL 20 2006

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. 7,023,056 B2

APPLICATION NO. : 10/723,331

ISSUE DATE : April 4, 2006

INVENTOR(S) : Liaw

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9, line 24; delete "having au NMOS" insert --having an NMOS--

Column 9, line 30; delete "short side a" insert --short side, a--

MAILING ADDRESS OF SENDER(Please do not use customer number below):

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**UNITED STATES PATENT AND TRADEMARK OFFICE
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